

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the Application of:

Ramanujan Valmiki, et al.

U.S. Serial No.: 10/666,702

Filed: September 19, 2003

For: VIDEO AND GRAPHICS SYSTEM
WITH MPEG SPECIFIC DATA
TRANSFER COMMANDS

Examiner: Dastouri, Mehrdad

Group Art Unit: 2621

Confirmation No.: 1993

ELECTRONICALLY FILED
On March 30, 2007

**LITIGATION STATEMENT PURSUANT TO MPEP § 2001.06(C) and INFORMATION
DISCLOSURE STATEMENT**

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The owner of record of the present application, Broadcom Corporation, is currently involved in a patent infringement action with Qualcomm, Inc., Case No. 06-CV-0660 B, pending in the Southern District of California.

Pursuant to MPEP 2001.06(c), the Applicants would like to bring to the attention of the U.S. Patent and Trademark Office that the subject matter currently being claimed in the present application may be related to at least one of the patents currently asserted by Broadcom in the above-mentioned lawsuit.

The Applicants have also attached with this electronic submission a completed PTO form PTO/SB/08A which has seven (7) pages. Copies of each printed reference listed in the PTO/SB/08A form are attached with this electronic submission. Applicants, however, have not

Patents or other references previously provided to or by the PTO in this application. Fifty-seven (57) references are attached in this electronic submission.

The references being submitted have been either cited, produced or relied upon by Qualcomm thus far during the above-mentioned lawsuit. This submission is in no way intended as an admission that the submitted references constitute prior art under any subsection of 35 U.S.C. §102 or §103. Applicant expressly retains the right to argue that any of the cited references are not indeed prior art or to take any actions necessary to remove any of the cited references from the available prior art.

The Examiner is requested to initial the attached PTO/SB/08A and return one copy to the applicants to indicate consideration of the attached references.

A fee in the amount of \$180 is believed to be due because:

- * The applicant(s) believe(s) that this statement and enclosures are being filed after the first Office action on the merits has been mailed by the PTO. The basis of this belief is that an Office action on the merits appears to have been received by the undersigned to date.

The Commissioner is hereby authorized to charge any fees which are presently required, or credit any overpayment, to Deposit Account No. 13-0017.

Respectfully submitted,

Date: March 30, 2007



Mirut P. Dalal
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500 West Madison Street, Suite 3400
Chicago, Illinois 60661
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 7

Complete if Known

Application Number	10/666,702
Filing Date	September 19, 2003
First Named Inventor	Ramanujan Valmiki
Group Art Unit	2621
Examiner Name	Dastouri, Mehrdad
Attorney Docket Number	17440US02

U.S. PATENT DOCUMENTS

Examiner Initial*	Cite No. ¹	Document Number	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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A1	4,020,332		04-26-1977	Crochiere et al.	
A2	4,367,466		01-04-1983	Takeda et al.	
A3	4,532,547		07-30-1985	Bennett	
A4	4,679,040		07-07-1987	Yan	
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A9	5,043,714		08-27-1991	Perlman	
A10	5,097,257		03-17-1992	Clough et al.	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450 Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. Send TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1800-786-9199) and select option 2.

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Sheet 2 Of 7

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Examiner Name	Dastouri, Mehrdad
Attorney Docket Number	17440US02

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		Number-Kind Code ² (If known)			
A38	5,621,869		04-15-1997	Drews	
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A40	5,625,611		04-29-1997	Yokota et al.	
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Sheet 3 Of 7

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Group Art Unit	2621
Examiner Name	Dastouri, Mehrdad
Attorney Docket Number	17440US02

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A80	5,940,089	08-17-1999	Dilliplane et al.		
A81	5,941,968	08-24-1999	Mergard et al.		
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A123		6,151,030	11-21-2000	DeLeeuw et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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B1	EP 0746116 A2	12-04-1996	Hiratsuka			
B2	EP 0 752 695	01-08-1997	O'Sullivan			
B3	EP 0840276 A2	05-06-1998	Benbassat			
B4	EP 0840277 A2	05-06-1998	Chauvel			
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B7	WO 94/10641	05-11-1994	Mical			
B8	WO 00/28518	05-18-2000	MacInnis			

OTHER ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C1	Plaintiff and Counterdefendant Qualcomm Incorporated's Preliminary Invalidity Contentions For U.S. Patent 6,501,480 and 6,570,579
	C2	Thomas Porter & Tom Duff, "Compositing Digital Images", Computer Graphics, Vol. 18, No. 3, July 1984, pp. 253-259
	C3	Akeley, K. & Jermoluk T., "High-Performance Polygon Rendering," 22 Computer Graphics No. 4 (ACM August 1988).
	C4	Apgar, B., et al., "A Display System for the Stellar Graphics Superconductor Model GS1000," 22 Computer Graphics No. 4 (ACM August 1988).
	C5	Asal, M.D., et al., "Novel Architecture for a High Performance Full Custom Graphics Processor," (IEEE 1989).
	C6	Awaga, M., et al., "3D Graphics Processor Chip Set," (IEEE December 1995).
	C7	Cox, M. & Bhandari, N., "Architectural Implications of Hardware-Accelerated Bucket Rendering on the PC," (Association for Computing Machinery SIGGRAPH/Eurographics Workshop 1997).
	C8	Cyrix Corporation Manual, copyrighted 1996, 1997, and all underlying development work.
	C9	Deering, M., et al., "The Triangle Processor and Normal Vector Shader: A VLSI System for High Performance Graphics," 22 Computer Graphics No. 4 (ACM August 1998).
	C10	Donovan, W., et al., "Pixel Processing in a Memory Controller," (IEEE Computer Graphics and Applications 1995).
	C11	Dutton, T., "The Design of the DEC 3000 Model 500 AXP Workstation," (IEEE 1993).
	C12	Foley, James, D., et al., "Introduction to Computer Graphics," (Addison-Wesley Publishing Co. 1994) ("Introduction to Computer Graphics").
	C13	Galbi, D., et al., "An MPEG-1 Audio/Video Decoder with Run-Length Compressed Antialiased Video Overlays," (1995 IEEE International Solid-State Circuits Conference).
	C14	Glaskowsky, Peter, "Cyrix Creates Ultimate CPU for Games," Microprocessor Report, December 8, 1997.
	C15	Glaskowsky, Peter, "Intel Displays 740 Graphics Chip: Auburn Sets New Standard for Quality - But Not Speed," Microprocessor Report No. 2, February 16, 1998.
	C16	Gwynnep, L., "Verite: A Programmable 3D Chip: Rendition Uses Internal RISC CPU for Versatility, Performance," 10 Microprocessor Report No. 6, May 6, 1996.

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OTHER ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
	C17	Hosotani S., et al., "A Display Processor Conforming to all DTV Formats with 188-TAP FIR Filters and 284 Kb FIFO Memories," (IEEE 1997 (Manuscript received June 13, 1997)).
	C18	Knittel, G. & Straber, W., "VIZARD - Visualization Accelerator for Realtime Display," (Association for Computing Machinery SIGGRAPH/Eurographics Workshop 1997).
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Complete if Known

Application Number	10/666,702
Filing Date	September 19, 2003
First Named Inventor	Ramanujan Valmiki
Group Art Unit	2621
Examiner Name	Dastouri, Mehrdad
Attorney Docket Number	17440US02

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
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